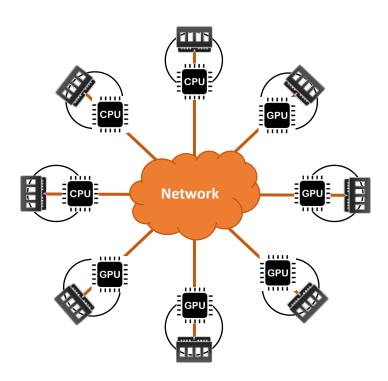
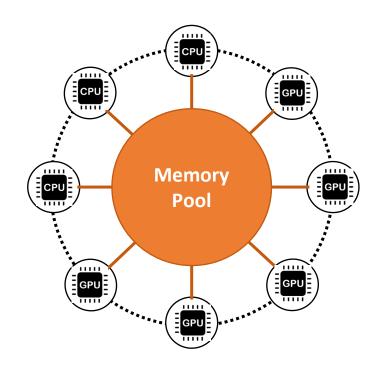




Memory Connects ALL



[Distributed AI System]



[Memory Centric Al Machine]





Benefit of Communication using Memory Pool

Low latency data communication

Reducing memory usage and the number of memory copies

cMPI: Using CXL Memory Sharing for MPI One-Sided and Two-Sided Inter-Node Communications (@ SC 2025)

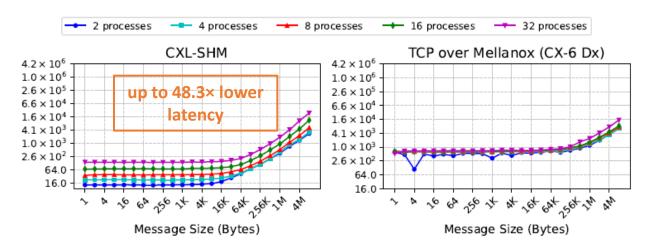
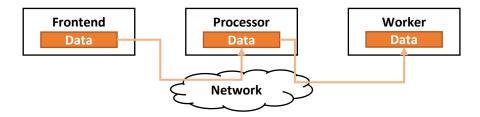
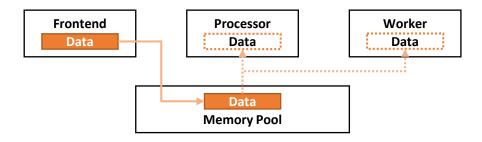


Figure 6: Latency of one-sided MPI communication.



[Data Communication-by-value using Network]



[Data Communication-by-reference using Memory Pool]

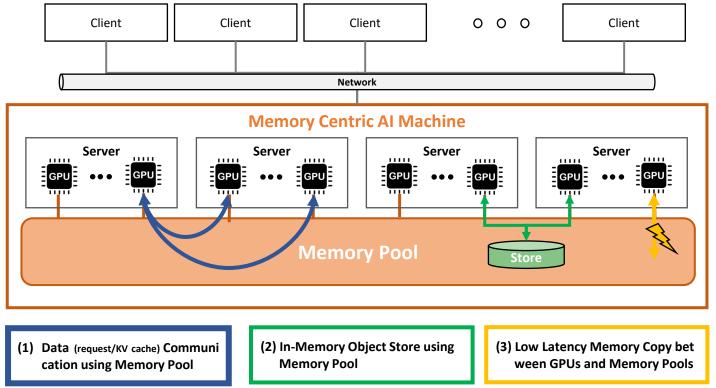




Memory Centric Al Machine

Memory Centric Al Machine Platform

Used for building and scaling distributed systems, especially those involving AI and HPC



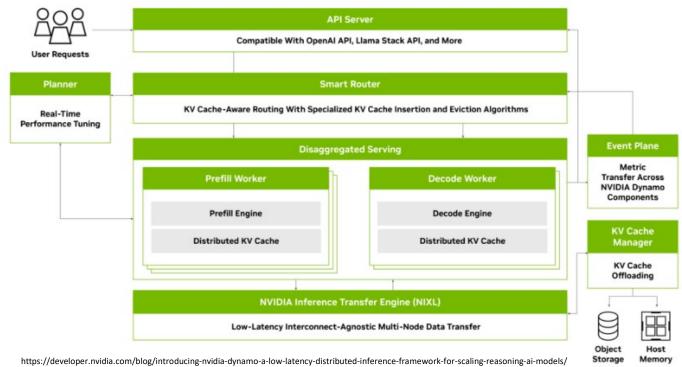




Dynamo LLM Serving

Low latency <u>distributed inference framework</u> for scaling reasoning AI models

- NVIDIA announced Dynamo at GTC 2025
- Open Source, High throughput and Low latency, Scalability, Distributed Inference, Modular Design



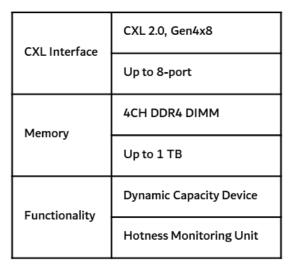
the Future of Memory ana storage



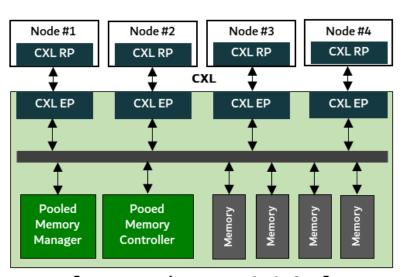
CXL Pooled Memory: Niagara

Built a Niagara HW/SW research platform, an FPGA-based CXL disaggregated memory prototype

- 2U memory appliance which can connect up to 8 CXL host servers (without CXL switch)
- Supports up to 4 channels of DDR4-DIMM (1TB)
- Supports DCD (Dynamic Capacity Device) and HMU (Hotness Monitoring Unit) feature in CXL spec. 3.x



[Niagara Specification]



[Niagara HW/SW Research Platform]

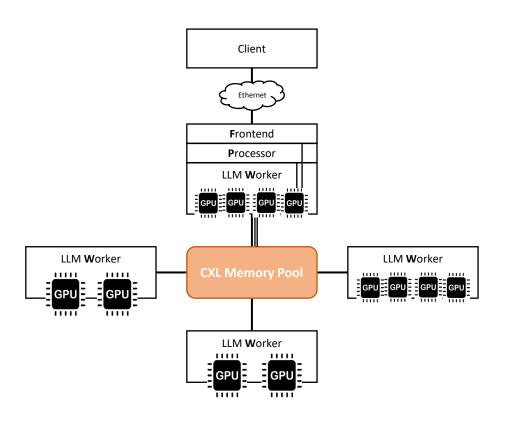


[Rack-Scale System with Niagara]





Dynamo LLM Serving with CXL Pooled Memory



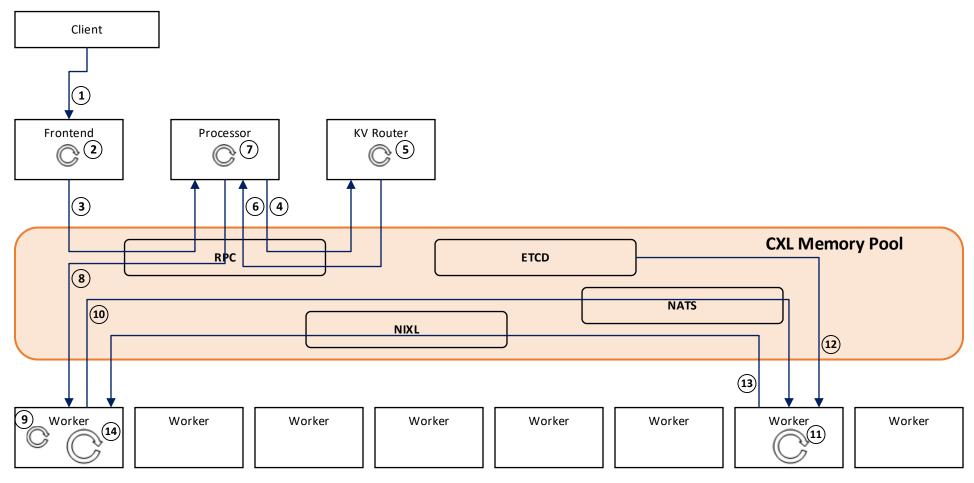








Dynamo LLM Serving with CXL Pooled Memory

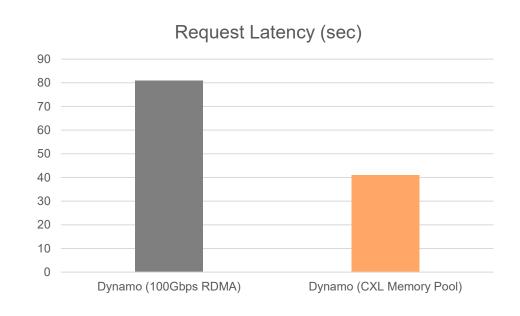


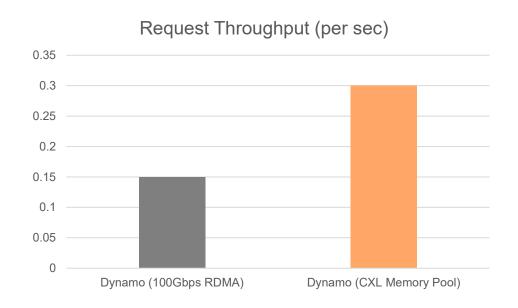


LLM Serving Performance

LLM Serving using CXL Pooled Memory can improve the LLM serving performance

DeepSeek-R1-Distill-Llama-8B, 15000 input tokens, 150 output tokens





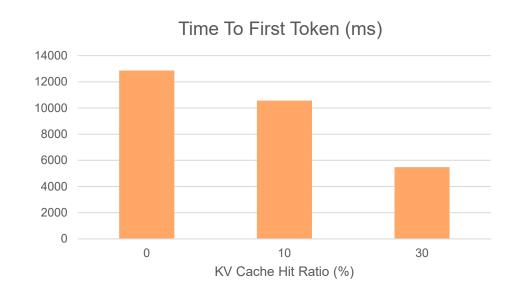


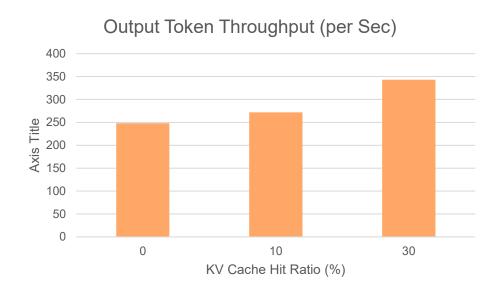


Memory Storing and Reusing

Communication using memory requires storing data to memory and loading data from memory

■ Reusing data from memory → Context / Prefix Cache









What's Next?

Memory Storing and Reusing

- Prefix / Context cache
- Multiple LLM models loading S-Lora

New systems using Memory Centric AI Machine

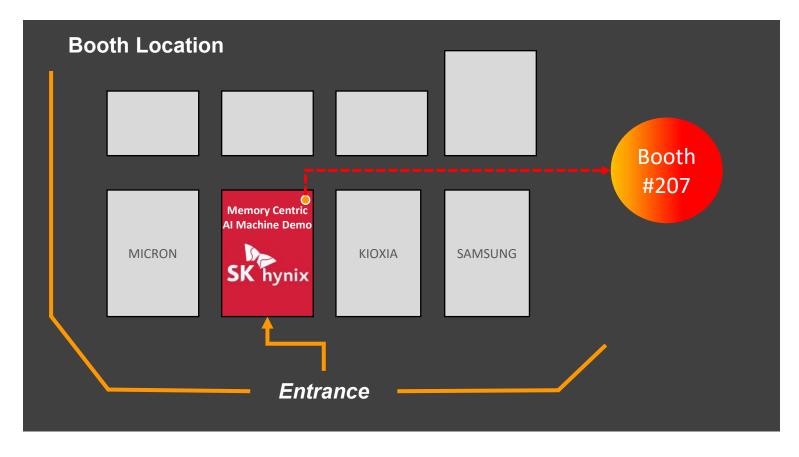
- Fine-tuning LLM Training System
- GNN Training System
- GPU based Quantum Simulation

Scalable Memory Centric Al Machine (to be revealed soon)





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