



the Future of Memory and Storage

CTO Vision Panel

A vision of the Future: Insights from Leading CTO's

August 6-8th, 2024

Santa Clara Convention Center

Al Fazio (Intel)

Al Fazio (Intel) is a Senior Fellow in the Foundry Technology Development Group, overseeing Intel's memory strategy. With over four decades of experience, Al has been instrumental in developing the industry's first commercial flash memory, multi-level-cell memory, and 3D XPoint storage class memory. His contributions have significantly shaped the memory and storage landscape, earning him over 30 patents and numerous awards for his technical achievements



Rita Wouhaybi (Solidigm)

Rita Wouhaybi (Solidigm) is an influential figure in AI and edge computing, currently driving AI strategies at Solidigm. With a background in Intel's CTO organization for network and edge, she has led groundbreaking projects in manufacturing, pharmaceuticals, and warehousing. Rita is passionate about democratizing AI and enhancing human capabilities through continuous learning and edge AI innovations, positioning her as a visionary leader in the field



Manoj Wadekar (Meta)

Manoj Wadekar (Meta) is a leading figure in Meta's technology division, focusing on advanced computing solutions and AI integration. His expertise in system architecture and data management has driven significant innovations in Meta's infrastructure, enhancing the performance and scalability of their computing systems. Manoj's forward-thinking approach and deep understanding of AI's impact on technology make him a key contributor to the panel



CJ Newburn

CJ Newburn (Nvidia) Distinguished Engineer driving HPC strategy and technical IO roadmap in NVIDIA GPU Cloud, focused on pushing the envelope for storage and networking programming models at scale. CJ is a community builder with a passion for building an ecosystem that extends the core capabilities of hardware and software platforms from HPC into AI, data science, and visualization. He tinkers with and leverages NVIDIA and vendor products in a lab packed with scaled compute, storage and networking gear to apply and extend new tech



Paul Borrill (DÆDÆLUS)

Paul Borrill (DÆDÆLUS) is a seasoned technology executive and entrepreneur with a distinguished multi-decade career. Paul was CTO of VERITAS Software during the 9/11 Emergency Data Recovery. He cofounded the IEEE Hot Interconnects Symposium and the Storage Networking Industry Association (SNIA). Paul has extensive experience in the design of fault tolerant & disaster resilient systems and infrastructures at NASA, Sun Microsystems, Quantum, VERITAS and Apple. Paul's obsession is making transactions fast, reliable and secure.



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CJ Newburn (NVIDIA)



Rita Wouhaybi (Solidigm)



Al Fazio (Intel)



Manoj Wadekar (Meta)



Russ Fellows [MODERATOR]



Paul Borrill (DÆDÆLUS)

Questions?

Please go to the Microphone