



Flash Memory Summit

Controllers and Flash Technology, Part 2: Error Correcting Codes

Organizer/Chairperson:

Erich F. Haratsch

Seagate



Agenda

Title	Speaker	Duration (mins)
Introduction	Erich Haratsch	4
Improving Quality of Service for 3D NAND SSDs Using LDPC Correction	Rino Micheloni, Lorenzo Zuolo	21
Intelligent Read Threshold Tracking to Mitigate Read Retry Requests	Oliver Hambrey	21
Ultra MMI: An LDPC Decoder that Doubles Throughput at End-Of-Life	Shiuan-Hao Kuo	21
Break		15
A Low-Cost LDPC Solution for Flash Memory	Osso Vahabzadeh	21
Using Machine Learning Techniques to Reduce SSD Costs	Cloud Zeng	21
Improving Waterfall Performance of low-cost FAID LDPC Decoders	David Declercq	21