

Overview of Persistent Memory: Media, Attachment & Usage

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Flash Memory Summit 2019 Santa Clara, CA

	- Top Electrode Free Layer - Turmel Barrier (Mgg - Reference Layers - Seeding Layers - Bottom Electrode	D) Top Electrode Phase Change Memory Cell Middle Electrode Selector Bottom Electrode	Top Electrode Resistive Memory Cell Middle Electrode Selector Bottom Electrode
	MRAM Magnetic RAM	PCRAM Phase Change RAM	ReRAM Resistive RAM
INITIAL DEPLOYMENT	Edge	Clou	Jd
KEY ATTRIBUTES	 ✓ Extremely fast (as fast as L3 cache) ✓ High density (one transistor per cell) ✓ Non-volatile 	 ✓ Faster than NAND ✓ Lower cost than DRAM ✓ Non-volatile ✓ Ideal for storage class memory 	
ENABLED BY	New materials, 3	D structures, Sub atomic-level p	precision films

Source: Gary Dickerson, Applied Materials; A.I Design Forum, July 9, 2019





Memory	Characteristic	Storage
Load/Store	Access	Blocks & Files
Yes	Coherent?	No
Yes, No	Persistent?	Yes
50ns – 500ns	Latency	>5us

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Usage: Value Proposition(s)

1. <u>PM = More Memory</u>

Pro: Existing applications want it Con: Compete directly vs. DRAM

2. <u>PM = Persistent & Coherent</u> Pro: Take full advantage of features

Con: Application needs to know



Upcoming FMS Sessions

• 3DXP

NewM-201; Wed 8:30am

• RRAM

NewM-202A; <u>Wed 4:35pm</u>

• MRAM

NewM-202B; Wed 4:35pm

• Life Beyond Flash

NewM-301A; Thu 8:30am



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