

How Form Factors Will Help Shape Tomorrow's Data Centric World

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The Dilemma of Defining The System of Tomorrow – Today

- Typical design point is 2 socket, 1U server
- Configurability is Critical
 - Needed today does not mean needed tomorrow
 - More stranded IOs = Opportunity lost, wasted \$\$\$
 - Ideal scenario: All precious IOs are utilized
- New technologies (e.g., FPGAs) increase the challenge







Challenges to Address











Need More NVM Sites

Support SSDs and MORE

less packages/SSD =
more dies/package =
lower yield/package

Legacy connectors have been SSD only.

Optimize for NVM

Legacy form factors in Enterprise and Datacenter based on HDDs or client SSDs.

Thermals and TCO Matter

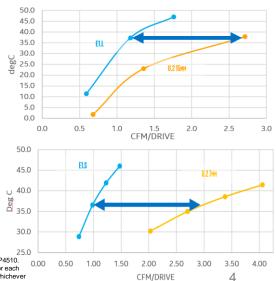
Legacy SSDs not thermally optimized. Airflow to CPU restricted.







- General purpose scalable connector
 - Flexible: Multiple orientations, widths, PCIe* Gen 5+ support
 - Supports interoperable specs (EDSFF, OCP Mezz, Gen Z, etc.)
- Break free of legacy to optimize for NVM
 - 50-100% increase in media package sites
- Improved thermal efficiency
 - 2-3x less airflow needed
 - Or support higher power devices



SFF-TA-1002

https://www.amphenol-icc.com/product-series/mini-cool-edge-0-60mm.html

Santa Clara, CA August 2018 Source – Intel. Comparing airflow required to maintain equivalent temperature of a 4TB U.2 from Intel® SSD DC P4500 to a 4TB "10 L" form factor for Intel® SSD DC P4500. 25 Source – Intel. Comparing airflow required to maintain equivalent temperature of an 8TB U.2 7mm Intel® SSD DC P4500 to a 4TB et U.2 from factor for Intel® SSD DC P4500. 25 Results have been estimated or simulated using internal analysis or architecture simulation or modeling, and provided for informational purposes. Simulation involves three drives for each form factor in a sheet metal representation of a server, 12.5mm pitch for "Ruler" form factor, 1000m elevation, limiting SSD on case temp of 70C or thermal throttling performance, whichever comes first. SC guard band. Results used as a proxy for airflow anticipated on EDSFF spec compliant "Ruler" form factor Intel® SD P4510.



Scalable Family for Different Usages



E1.L (SFF-TA-1007)

- 318.75 x 38.4 mm
- Supports > 40W
- Up to 48 Standard NAND sites

- Same Protocol: NVMe
- Same Interface: PCIe
- Same Connector: SFF-TA-1002
- <u>Same</u> Pinout and Functions (hot plug, serviceable)
- <u>Different</u> Usages, <u>Same</u> Expectations!



E1.S (SFF-TA-1006)

- 111.5 x 31.5 mm
- Supports >12W
- Up to 12 Standard NAND sites

E3 (SFF-TA-1008)

- (104.9/142.2) x 76mm
- Supports up to 70W
- Up to 48 Standard NAND sites



Build Your Future with EDSFF

- EDSFF is THE form factor for the next decade for SSDs
 - Higher power, higher bandwidth, better thermals and better density
- Future proof you next design with the common connector
 - SSDs, NICs, FPGAs, Storage Class Memory, Accelerators, ...

For more information: <u>http://www.snia.org/sff/specifications</u>

- <u>Connector</u>: SFF-TA-1002: Card Edge multilane protocol agnostic connector
- E1.S formfactor: SFF-TA-1006: Enterprise and Datacenter 1U Short SSD Form Factor
- <u>E1.L formfactor</u>: SFF-TA-1007: Enterprise and Datacenter 1U Long SSD Form Factor
- <u>E3 formfactor</u>: SFF-TA-1008: Enterprise and Datacenter 3" Media Device Form Factor
- <u>Pin list/other</u>: SFF-TA-1009: Enterprise and Datacenter SSD Pin and Signal Specification