

What is Gen-Z?

Gen-Z is an open-systems interconnect designed to provide **memory-semantic access** to data and devices via **direct-attached, switched or fabric topologies**.

Gen-Z allows business and technology leaders to **overcome current challenges** with existing computer architecture and provide **opportunities for innovative solutions** that are **open, simple and cost-effective**.

The **Gen-Z Core Specification 1.0** was released to the public in 2018 and enables **silicon providers and IP developers** to develop products enabling Gen-Z technology solutions.

Gen-Z's memory-centric, standards-based approach focuses on providing an **open, reliable, flexible, secure and high-performance architecture** for housing and analyzing the incredible amount of information at the edge coming into the data center.

**Learn more about
Gen-Z technology
and download**

Core Specification 1.0 today!

www.genzconsortium.org



About the Gen-Z Consortium

- Comprised of leading technology companies dedicated to creating and commercializing the Gen-Z technology
- Formed in 2016 by founding members AMD, ARM, Broadcom, Cray, Dell EMC, Hewlett Packard Enterprise, Huawei, IDT, Micron, Samsung, SK Hynix, and Xilinx
- Our mission is to deliver a suite of specifications to enable Gen-Z to be integrated into a wide variety of solutions and applications
- The Consortium has grown rapidly, with more than 50 members and more joining!

Join us today!

Visit www.GenZConsortium.org
for membership information.

GEN Z



“Gen-Z provides something akin to a hybrid fabric, combining a memory bus, a processor bus, a peripheral device bus, and a network interconnect. It also represents a paradigm shift in the way systems access memory.” – Top500