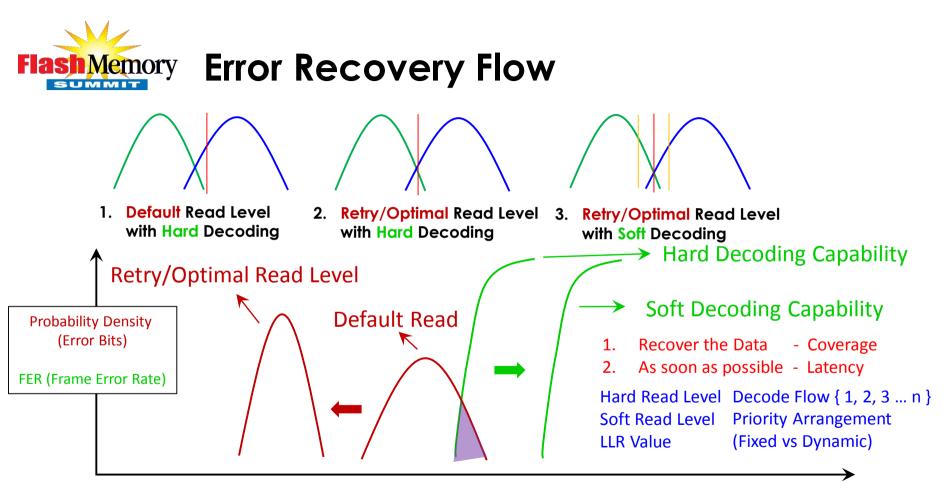




Novel Error Recovery Architecture Based on Machine Learning

Cloud Zeng LITEON/Storage/NVM Lab

Flash Memory Summit 2018 Santa Clara, CA



Error Bits Count/Chunk Size

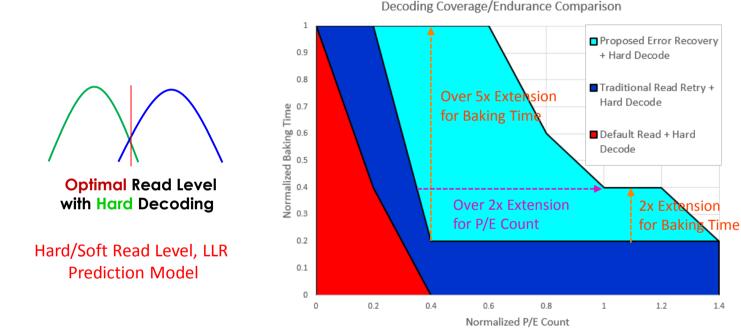


Error Recovery Scheme with ML

Category	ltem	Description	Remark	
P/E	Cycle	0, 1000,~		
	Temperature	(Random)		
	Dwell	(Random)		
Test Item	Data Retention	0, 1, ~ (Days)	Room/High Temperature	
	Read Disturb	0, 1000, ~	High Temperature	
	Cross-Temperature	HT/LT Write – LT/HT Read		

- An Error Recovery Scheme is developed by Machine Learning
- This Scheme can be applied to variant operation condition (combination of {PE, DR, RD, Temperature, Cross-Temperature})
- This Scheme can extend the endurance and reduce the latency

Memory Endurance with Hard Decoding



- Our Error Recovery Scheme use ML to find Optimal Parameters for variant operation conditions (combination of {PE, DR, RD, Temperature})
- 5x Extension for Baking Time & 2x Extension for P/E Count

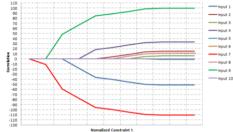


Prediction Model - Optimal Read Level

Example: Data Collection

	Input Para 1	Input Para 2	Input Para 3	Input Para 4		Input Para 6	Optimal HD Read Level	
Data 1	1100	589	1794	6322	1000	1000	6	
Data 2	932	908	1503	7849	500	500	-5	
Data N	990	842	1894	5692	300	400	3	

Feature Selection



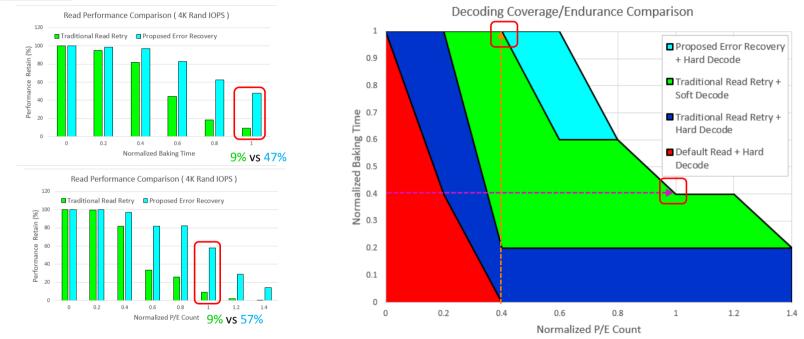
- What's the Optimal HD Read Level after n Days/Weeks?
- Input Parameters:
 - P/E Cycle, Retention Time, Read Count, Temperature, Dwell ... Program/Erase Time, Histogram
- **Regression Problem**:
 - Ordinary Least Square(OLS) Regression
 - Ridge Regression (Hoerl and Kennard, 1970)
 - Other Regression Analysis can be used to solve this problem

Throughput/IOPS Comparison

Flas

Memory

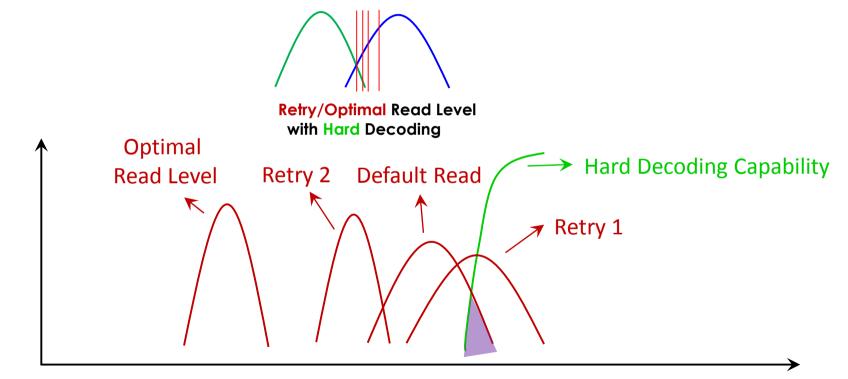
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 Proposed Error Recovery Scheme always has less read latency compared with Traditional Error Recovery Scheme



Optimized Read Retry Sequence

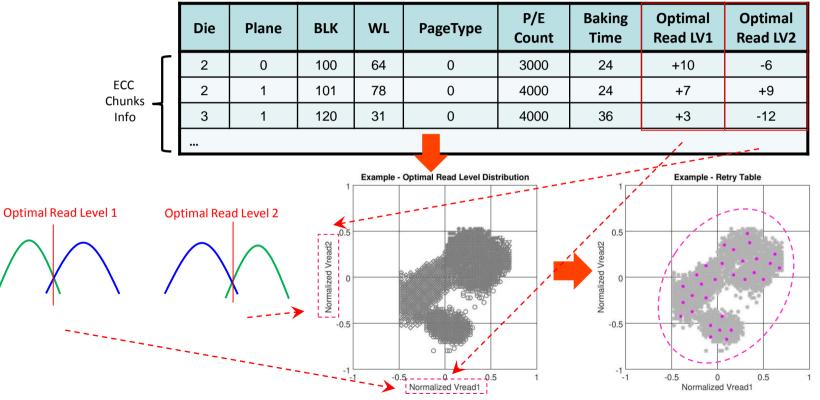


Error Bits Count/Chunk Size



Read Retry Table – Clustering

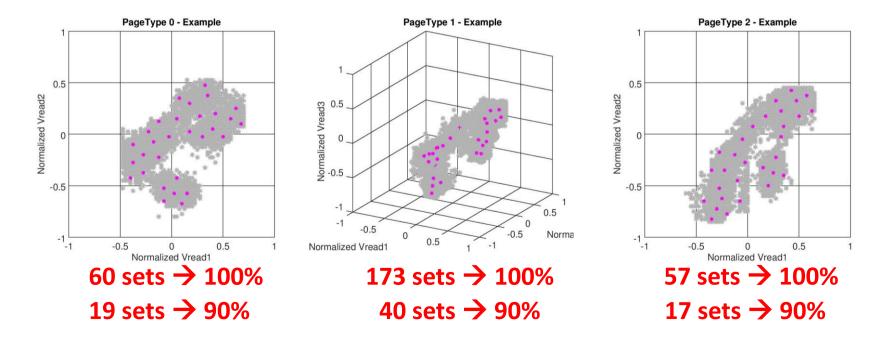
Billions of ECC Chunks Info were collected over dice under different failure mode





Read Retry Table – Coverage

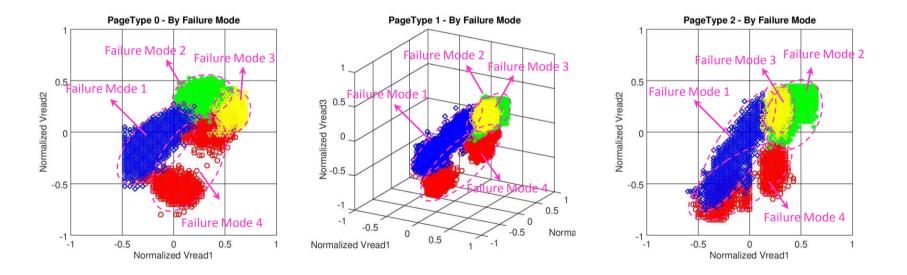
How many Retry Tables are required to cover the following case ?





Reduced Retry Table – Coverage

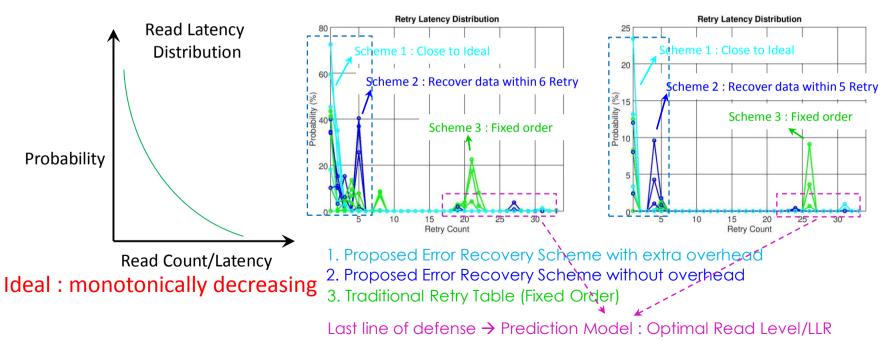
Find some indexes to reduce retry tables without Coverage Loss



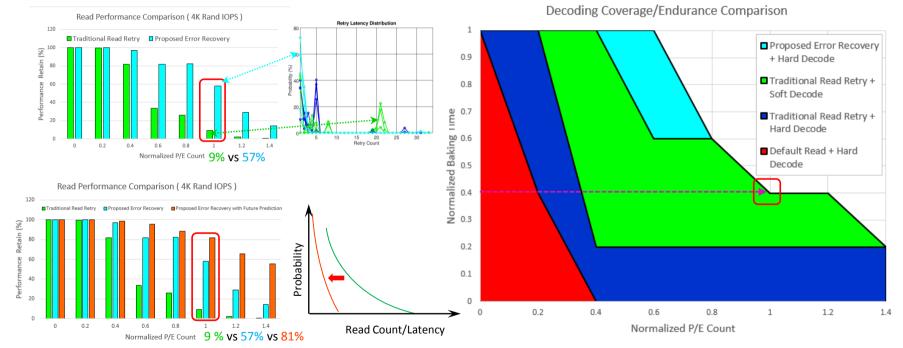


Reduced Retry Table – Latency

Change Default Read Level and the Priority of Retry Table dynamically



Memory **Throughput with Future Status Prediction**



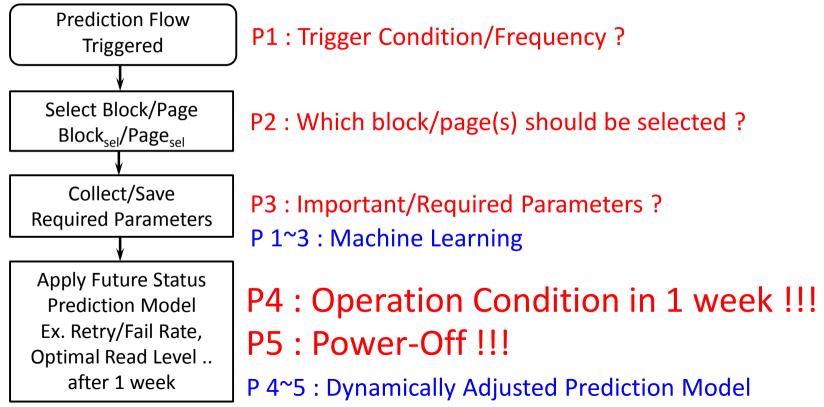
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Read Performance Drop can be further reduced with Future Status ۲ Prediction



Difficulty For Future Status Prediction





- Current NAND Flash Endurance can be Greatly Extended
 - Optimal Parameters : Retry/Optimal Read Level, LLR
 - Powerful Recovery Flow : Soft Decode, Future Status Prediction..
 - The key point is ... QoS (Quality of Service)
- Error Recovery Scheme based on Machine Learning
 - Optimized Read Retry Sequence
 - Optimal Read Level, LLR Estimation/Prediction Model
 - Future Status Prediction Model
- New Error Recovery Architecture
 - Adjust Error Recovery Flow based on failure mode/operation condition
 - Dynamically Adjusted Estimation/Prediction Model



THANK YOU! Any questions?



Come by LITE-ON Booth# 621 for Demos! Learn about Machine Learning & the latest SSD Technology Get a chance to win special prizes!

