# QLC Flash: Meeting the Challenges of the New Data Economy

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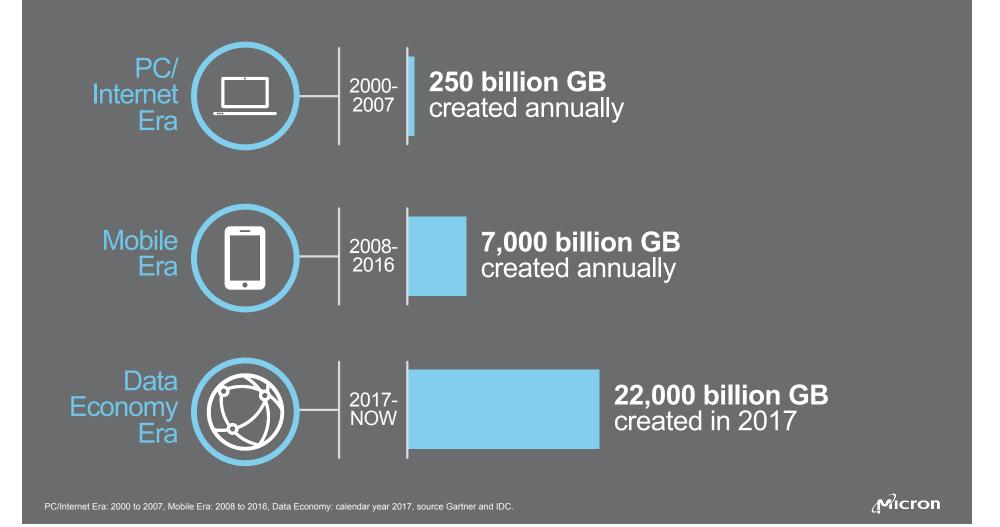
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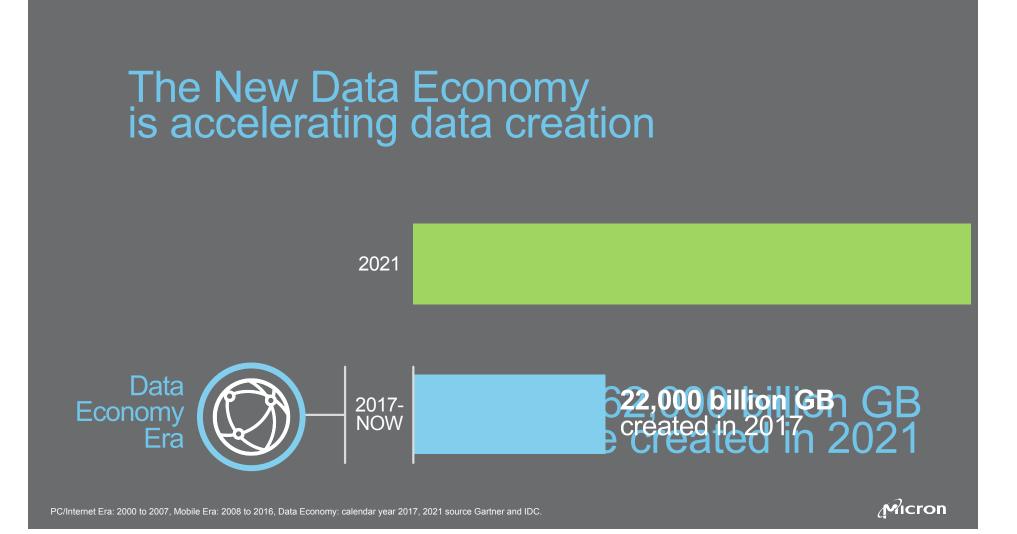












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# Client PC

- >80% PC SSD adoption anticipated
- >2X expected average capacity increase

Source: Micron, 2021 data from IDC WW 2017-2022 SSD Market Forecast Update





# **Micenite**PC

- \$80% retrootd and between the second and second average
   \$20% retrootd average
   \$20%

Source: 2021 data from Micron



## Matoileotive

- VebX: leesteroorders beorjeteiteiteithteinicreræsese twit2:050-per car
- 221 Xillion essesses or sDRAM and a 3 XeN AND Doger abtome at intecipatted

Source: 2020 data from http://www.automotivesensors2017.com/



### Ateromotiveings

- Sehiste datasgrowing atcjextedisiniessetate
  Sinagesensors
  121 billion bensors anticipatenderating automotive data

Source: 2020 data from equalum.io: "The Future of Big Data is Here"





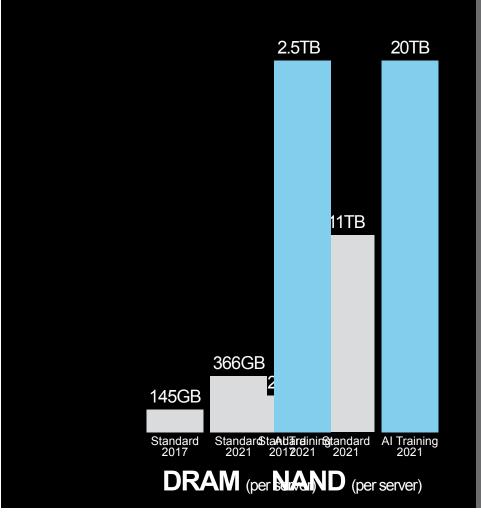
#### DtataeCerTteings

- Sensor data growing at 50X business data
- 3 image sensors for every human anticipated



# A requires memory and storage to deliver results

Harvesting Big Data to Create Immense Value



#### Al Workloads Unleash the Need For More Memory & Storage

AI Training Impact in 2021 vs. Standard Server Config

6.8X.8X DRAMNAND

Source: Micron



#### Al Workloads Unleash the Need For More Memory & Storage

Systems are already capable:

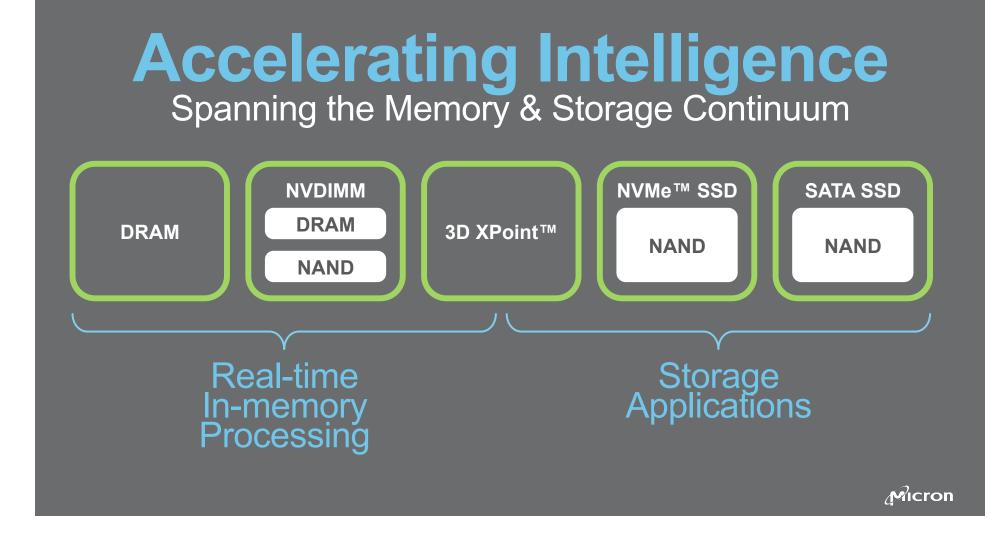
1.5TB 30TB DRAM NAND

#### AI & DL Are Changing the IO Patterns of the Data Center



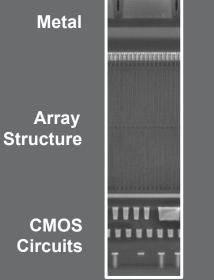
Traditional Data Center IOVS.IO pattern, Deep Learning for AI4:1 read-to-write ratio5000:1 read-to-write ratio

Source: EnterpriseStorageforum.com: "Data Storage, AI, and IO Patterns"

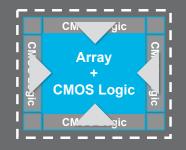






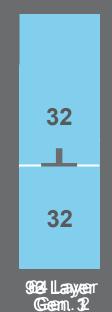


Top Down View CMOS under Array



Enabled Cost Reduction & Performance Improvements





- 50% increase in layers
- Sets write bandwidth benchmarks
- Array stacking for the 2nd time
- Uses CMOS under array for the 3rd time

# **Russ Meyer**

Micron Corporate VP of Non-Volatile Memory Integration

# 4th Gen 3D NAND: Optimized for performance and scaling.



Micron Developed Micron's solely developing Replacement Gate (RG) technology



Uniquely Designed Incorporates unique combination of CMOS under array with novel charge trap cell technology



 Performance Driven

 Targets industry-leading die

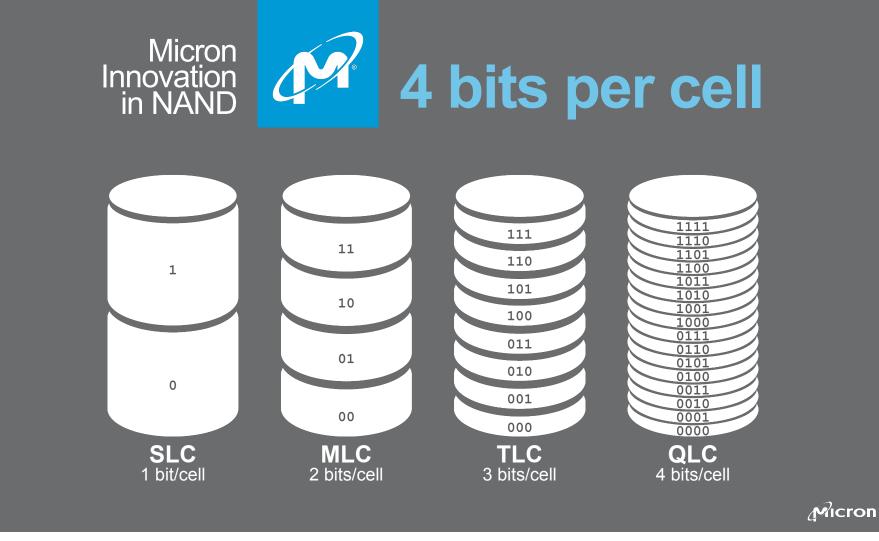
 size & performance

Targets industry-leading die size & performance

# 4th Gen 3D NAND: Optimized for performance and scaling.

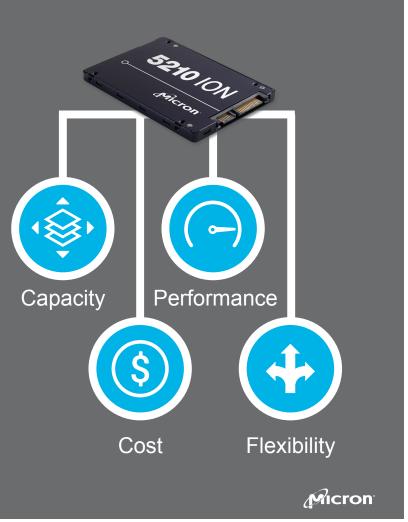
Significant Improvements Optimized To Meet Future Needs Of Multiple End Markets





# Now Shipping the **Industry's First** QLC SSD

Read-Intensive Enterprise Workloads at 7X lower TCO



QLC refers to 4 bits per cell. TCO vs. 7200 RPM HDD for same performance

#### The QLC Workload Advantage



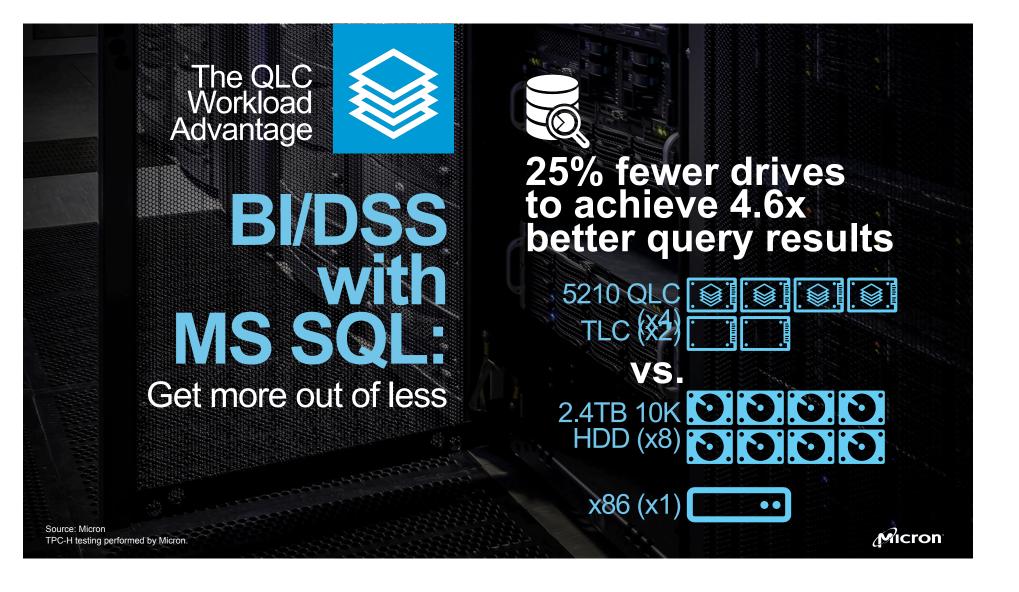
Right-sized, cost-effective performance for the top workloads of today & tomorrow



Real-time analytics & read-centric data stores



**Content delivery** & distribution



#### The QLC Workload Advantage



# No SQL Cassandra

Get nearly 4x the IO performance with a fourth of the drives 4X 2.4TB 10K HDD 1,776 operations per second

#### .♥.] **1X** 7.68TB 5210

**6,900** operations per second

**3.9X** more operations per second **10%** power savings with 5210

Source: Micron YCSB testing performed by Micron.

#### The QLC Workload Advantage



#### **3 Node Ceph Cluster**



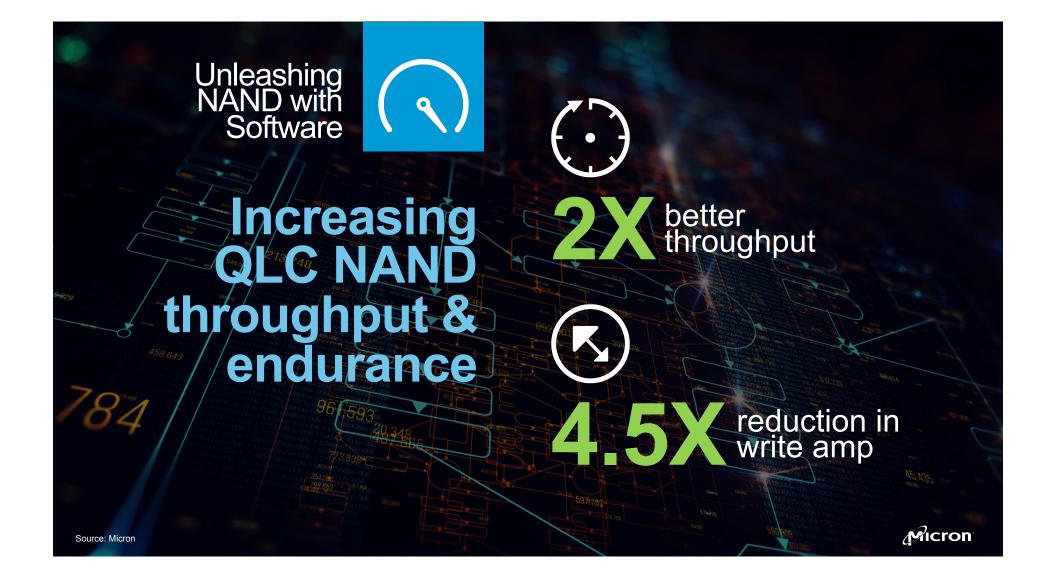
70Gbps

object read output

**Store:** Ceph with QLC is a cost-effective media streaming solution

Micron

Source: Micron RADOS Bench object storage benchmark performed by Micron







Micron/storage stack: Designed to optimize database application Increased Operations per Second More than 8X improvement



Reduced Latency 95%+ latency reduction

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Lower System Write Amplification More than 8X improvement

Reduced Power Consumption 7X improvement

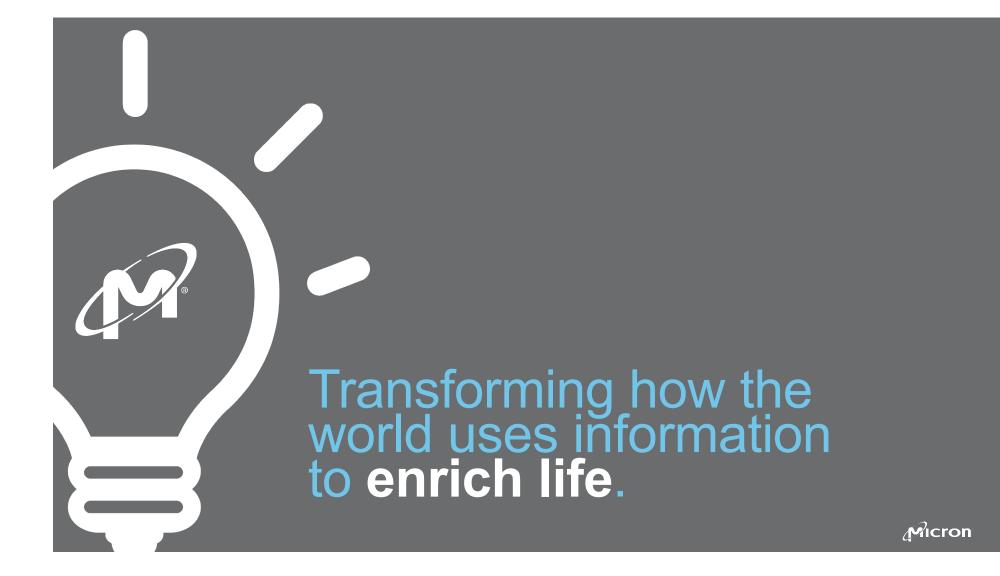
Source: Micron





Engaging with customers at a different level Researchers accelerated speeds on our weather sampling database by more than 2X ... which will enable more comprehensive and fine-grained understanding of climate conditions and natural hazards.





# Join Micron at FMS 2018:

#### Booth 407

#### **Breakout sessions:**

QLC Is the Best Way to Replace Enterprise HDDs Today | 3:40-4:45 p.m. | Great America Ballroom J

Meeting the Storage Needs of 5G Networks Today | 4:55-6:00 p.m. | Great America Ballroom J

QLC and Mixed Mode SSDs Require Deep FTL-Tuning Today | 3:40-6:05 p.m. | Great America Meeting Room 1

New Flexible Form Factors for Enterprise and Data Center SSDs Tomorrow | 8:30-10:50 a.m. | Great America Ballroom K

Reception: 7 p.m. tonight in the Terra Courtyard

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